

SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI
Hanuman Vyayam Prasarak Mandals's
College of Engineering & Technology, Amravati
Course: Information Technology
B.E Four year Semester (Information technology) Summer 2020 Exam
Subject: 5IT03 COMPUTER ARCHITECTURE & ORGANIZATION

Assignment for ONLY BACKLOG STUDENTS

Instructions

- 1) Solve Any Two Questions
- 2) Each Question Carries 10 marks

QI) Solve the following.

1. What is addressing mode? 2M
2. Explain microprogrammed control unit. 2M
3. What do you mean by Interrupt? 2M
4. Explain the Cache Memories. 2M
5. What is the need of Booth's Algorithm? 1M
6. Explain the working of scanner. 1M

QII) Solve the following.

1. Explain Big- endian & Little-endian Assignment. 2M
2. What do you mean by Micro-instruction? 2M
3. Explain Interrupt Nesting. 2M
4. Explain Virtual Memory Organization. 2M
5. Explain 4-bit carry look ahead adder. 1M
6. Explain Magnetic Disc System. 1M

QIII) Solve the following.

1. What is branching? 2M
2. Explain Hardwired control unit. 2M
3. What is DMA? 2M
4. State various Mapping Techniques? 2M
5. Explain Booths algorithm for multiplication of two numbers. 1M
6. Differentiate half duplex & full duplex. 1M

QIV) Solve the following.

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| 1. Explain basic functional units of computer. | 2M |
| 2. Explain the single bus organization. | 2M |
| 3. Explain the term Processor Bus. | 2M |
| 4. Explain Static Memories. | 2M |
| 5. Explain Multiplication of Positive Numbers. | 1M |
| 6. Explain working of CD ROM. | 1M |