

SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI
Hanuman Vyayam Prasarak Mandals's
College of Engineering & Technology, Amravati
Course: Information Technology
BE Four year Third Semester (Information technology) Summer 2020 Exam
Subject: 3IT05 ASSEMBLY LANGUAGE PROGRAMMING
Assignment for ONLY BACKLOG STUDENTS

Instructions

- 1) Solve Any Two Questions**
- 2) Each Question Carries 10 Marks**

Q I) solve the following

- 1 Evaluate the decimal equivalent for each of the following. integer binary numbers :i) 00010101 1 M
ii)10.110
- 2 Explain XLAT 1 M
- 3 Explain loop handling instructions. 2 M
- 4 What are the advantages of Macro over subroutine? 2 M
- 5 Draw I/O address space of 8086 microprocessor. 2 M
- 6 Explain operation of Interrupt instruction :(i) CLI (ii)STI (iii) IRET 2 M

Q II) Solve the following

- 1 Draw micro architecture of the 8088/8086 processor. 2 M
- 2 Explain (i)Mov (ii) XCHG 2 M
- 3 Explain flag control instructions. 2 M
- 4 Why Dynamic RAM needs to be refreshed? 1 M
- 5 Draw internal organization of PPI 8255. 1 M
- 6 How interrupt handling mechanism work in 8086? 2 M

Q III) Solve the following

- 1 Sketch software model of 8088/8086 microprocessor. 2 M
- 2 Explain the following instruction : (i) ADD (ii) ADC (iii) NEG 2 M
- 3 Give logical symbol, truth table and format for the following instruction 2 M
(i) AND (ii) NOT (iii) XOR
- 4 Explain different types of ROM. 2 M
- 5 Draw isolated I/O interface with 8088. 1 M
- 6 Explain context switch mechanism in interrupt. 1 M

Q IV) Solve the following

- 1 State and Explain data types supported by 8088/8086. 2 M
- 2 Explain the following instruction : (i) AAA (ii) CMP 2 M
- 3 Explain Stack related instructions. 2 M
- 4 Compare static RAM and Dynamic RAM 2 M
- 5 Draw maximum mode 8086 system I/O interface. 1 M
- 6 Draw the internal architecture of 8259 PIC 1 M