

**Sant Gadge Baba Amravati University, Amravati Summer 2020 Exam**  
**H.V.P.Mandal's College of Engineering and Technology, Amravati**  
**Course: Computer Science & Engineering**  
**BE Four Year Seventh Semester (Computer Science & Engineering) Summer 2020 Exam**  
**7KE03 MICROPROCESSOR AND INTERFACING**  
**ONLY FOR BACKLOG STUDENTS**

**Marks: 20**

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**Note:**

**1.Solve any two questions**

**2.Each questions carries 10 marks**

**Question No. 1(10Marks)**

A	Draw the pin diagram of 8086	2
B	Explain any 4 instruction set of 8086	2
C	Draw 8255 pin diagram.	2
D	Draw pin diagram of 8259.	1
E	Draw The basic block diagram of 8254	2
F	Draw pin diagram and pin description of 8237.	1

**Question No. 2(10Marks)**

A	Explain the function of various pin's 8086.	2
B	Explain instruction of data transfer group of 8086.	2
C	Explain BSR mode of 8255.	2
D	Explain cascade mode of 8259.	2
E	What is the use of different timer in 8254?	1
F	Draw the internal architecture of 8237.	1

**Question No. 3(10Marks)**

A	Explain I/O addressing mode of 8086.	2
B	Explain instruction of data logic group of 8086.	1
C	Draw interfacing diagram of 8086 and I/O interfacing through 8255.	2
D	How many priority modes are available in 8259?	1
E	Draw the format of Control Word of 8254.	2
F	Explain USART 8251.	2

**Question No. 4(10Marks)**

A	Explain bus cycle of 8086 organization.	2
B	Explain instruction of data branch group of 8086.	2
C	Write down mode of operation of 8255.	2
D	Explain types of interrupts in an 8086 microprocessor.	2
E	Draw the pin diagram of 8254.	1
F	Draw pin diagram of 8251.	1